

6426975

Fig.1

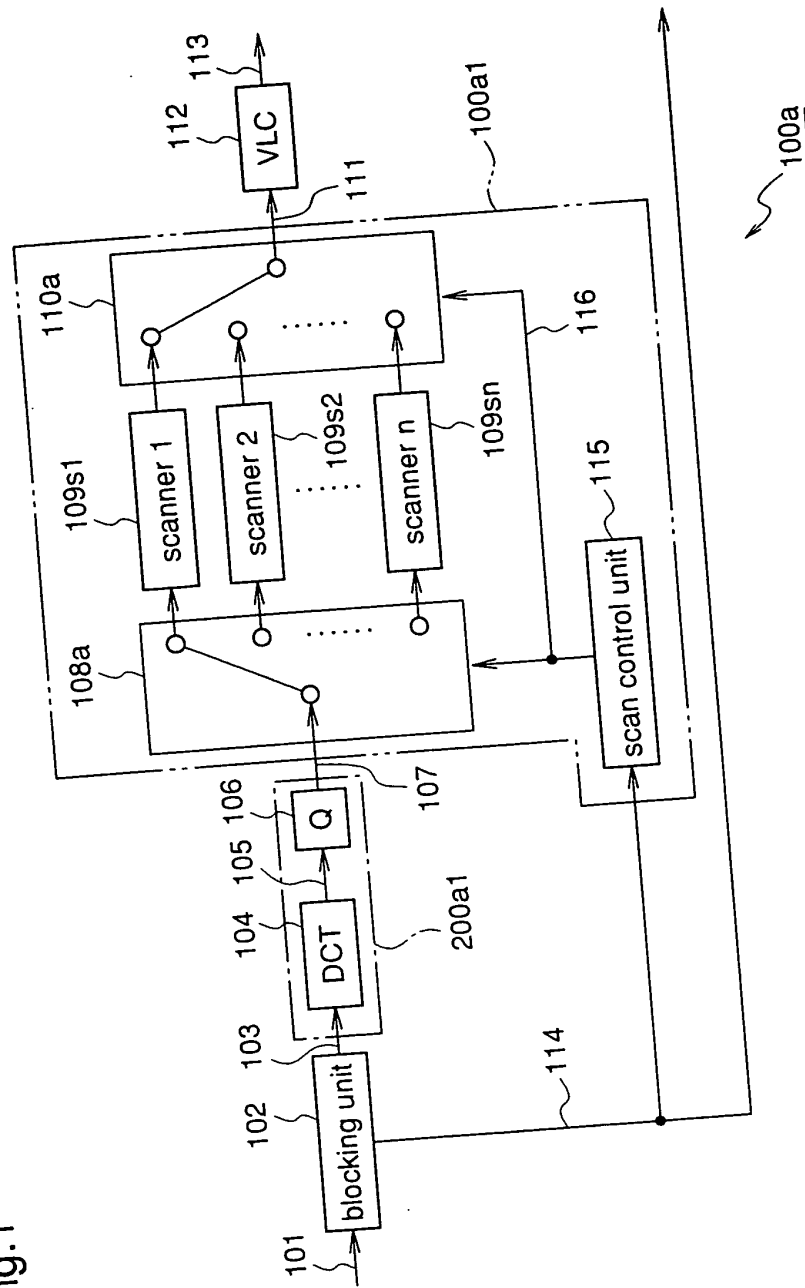


Fig.2 (a)

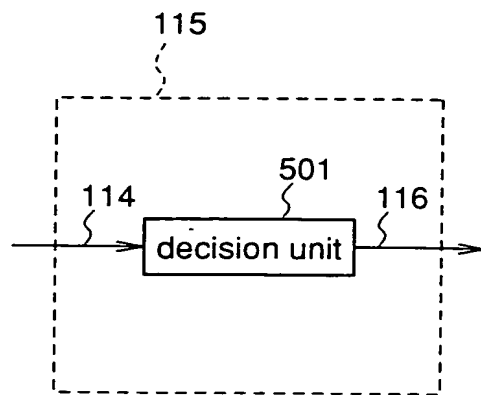


Fig.2 (b)

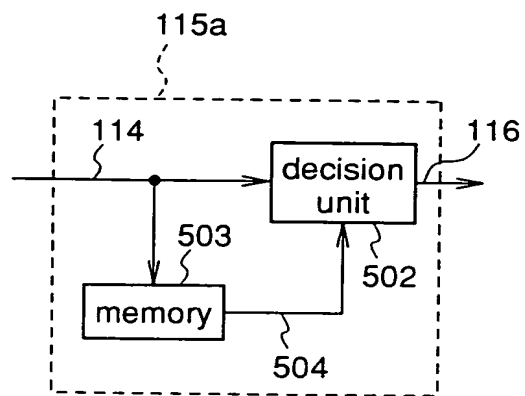


Fig.2 (c)

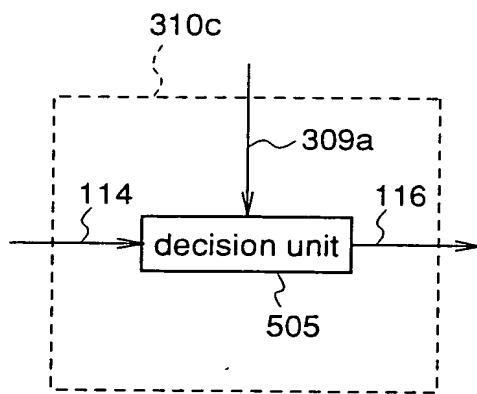
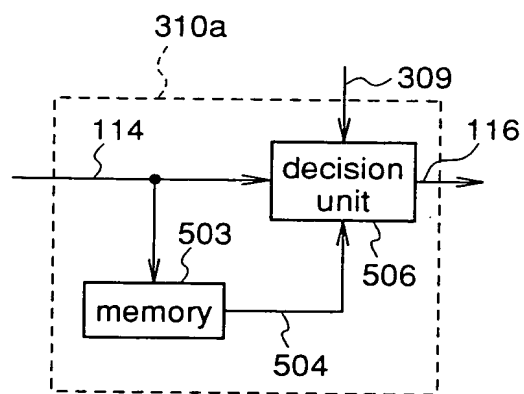


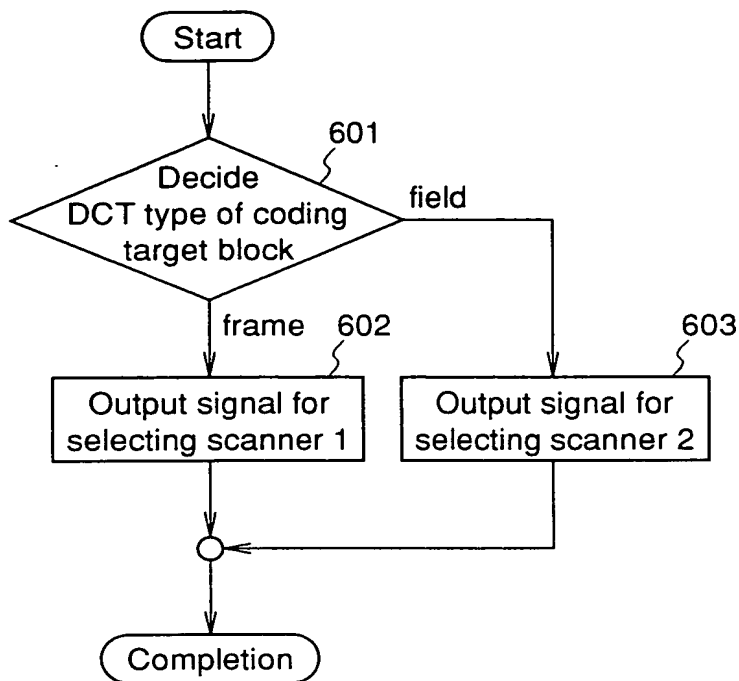
Fig.2 (d)



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APPROVED	O.G. FIG.	
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Fig.3



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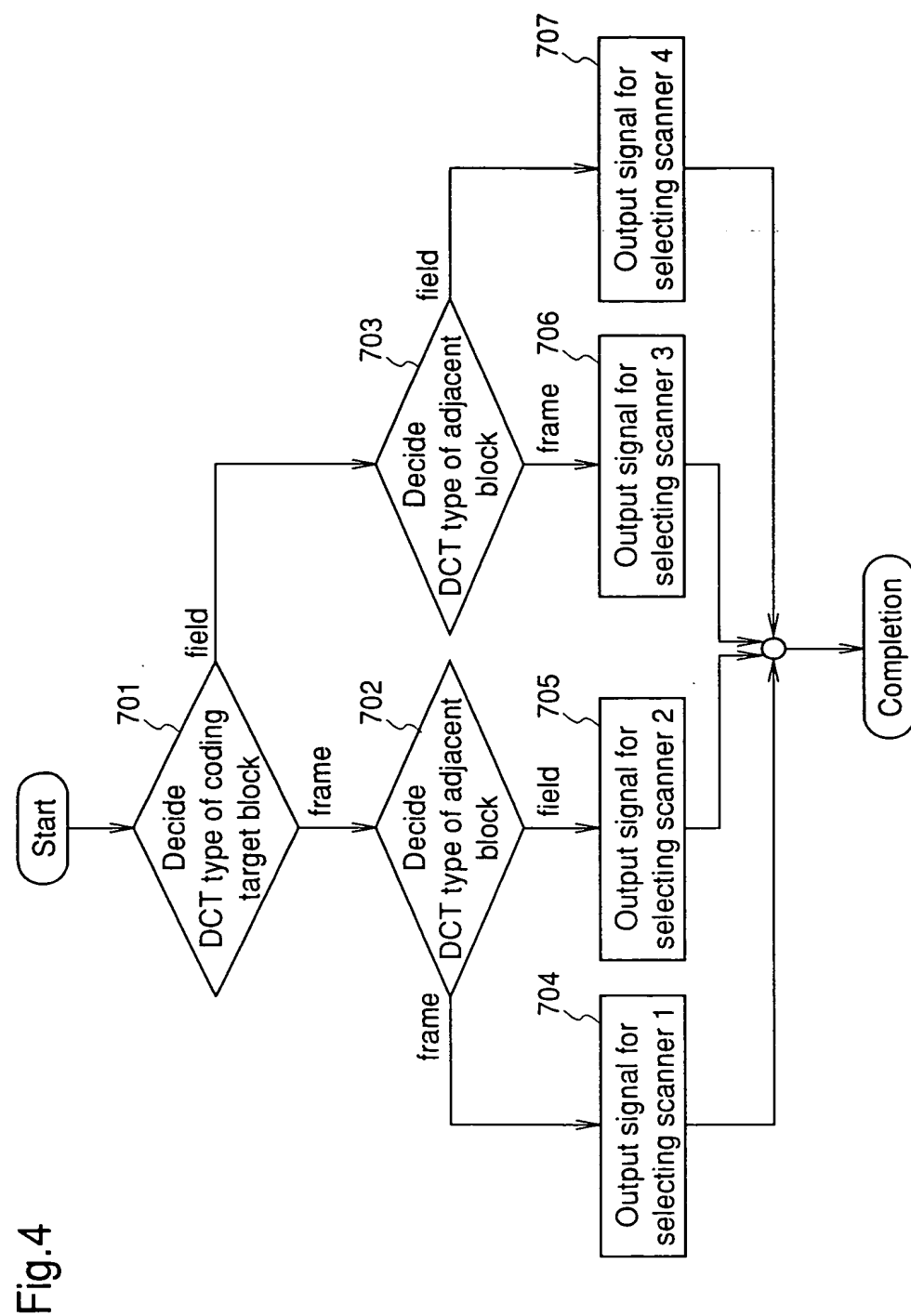


Fig. 4

560243 T668T60

360220-1653160

Fig.5

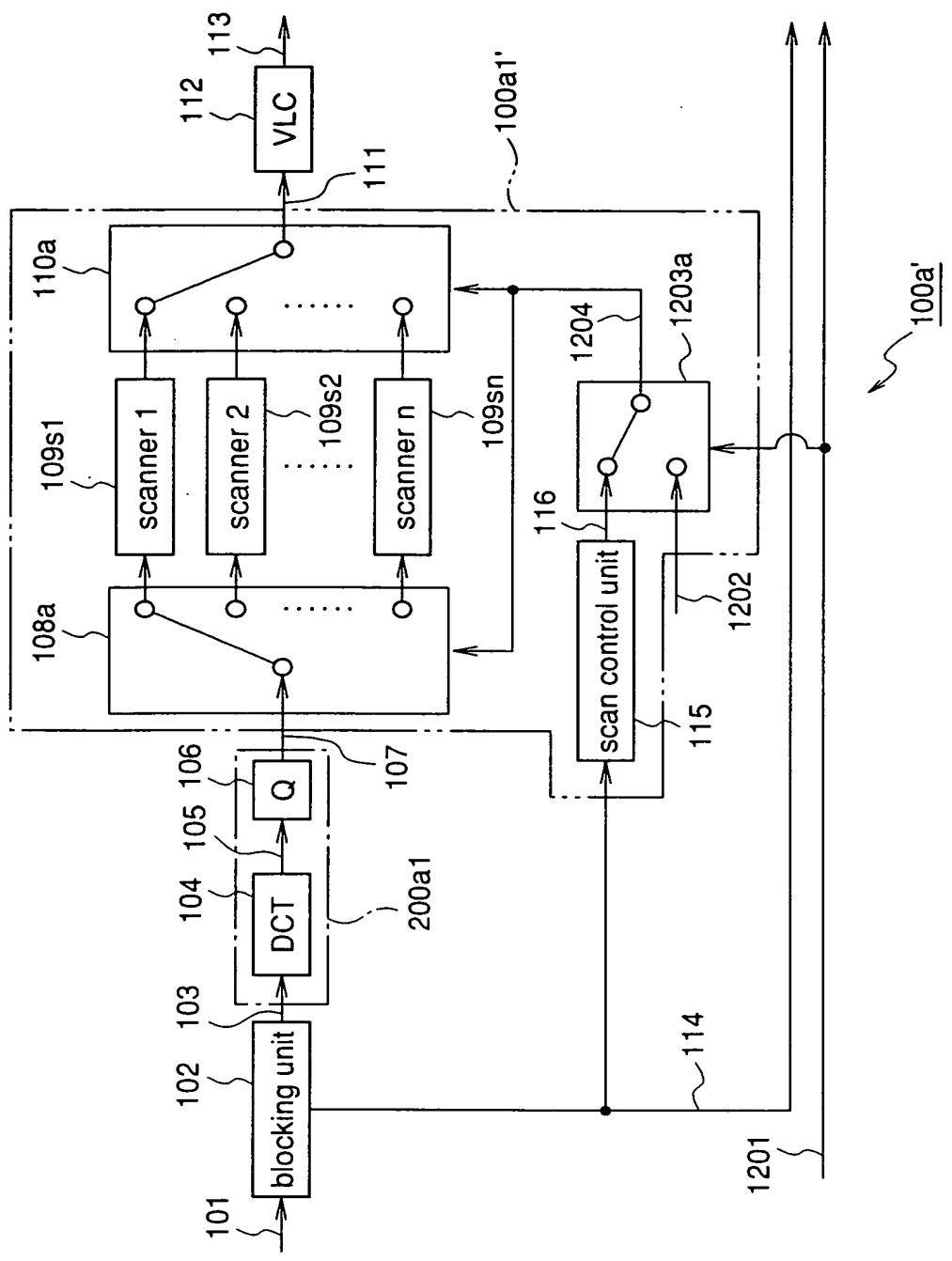
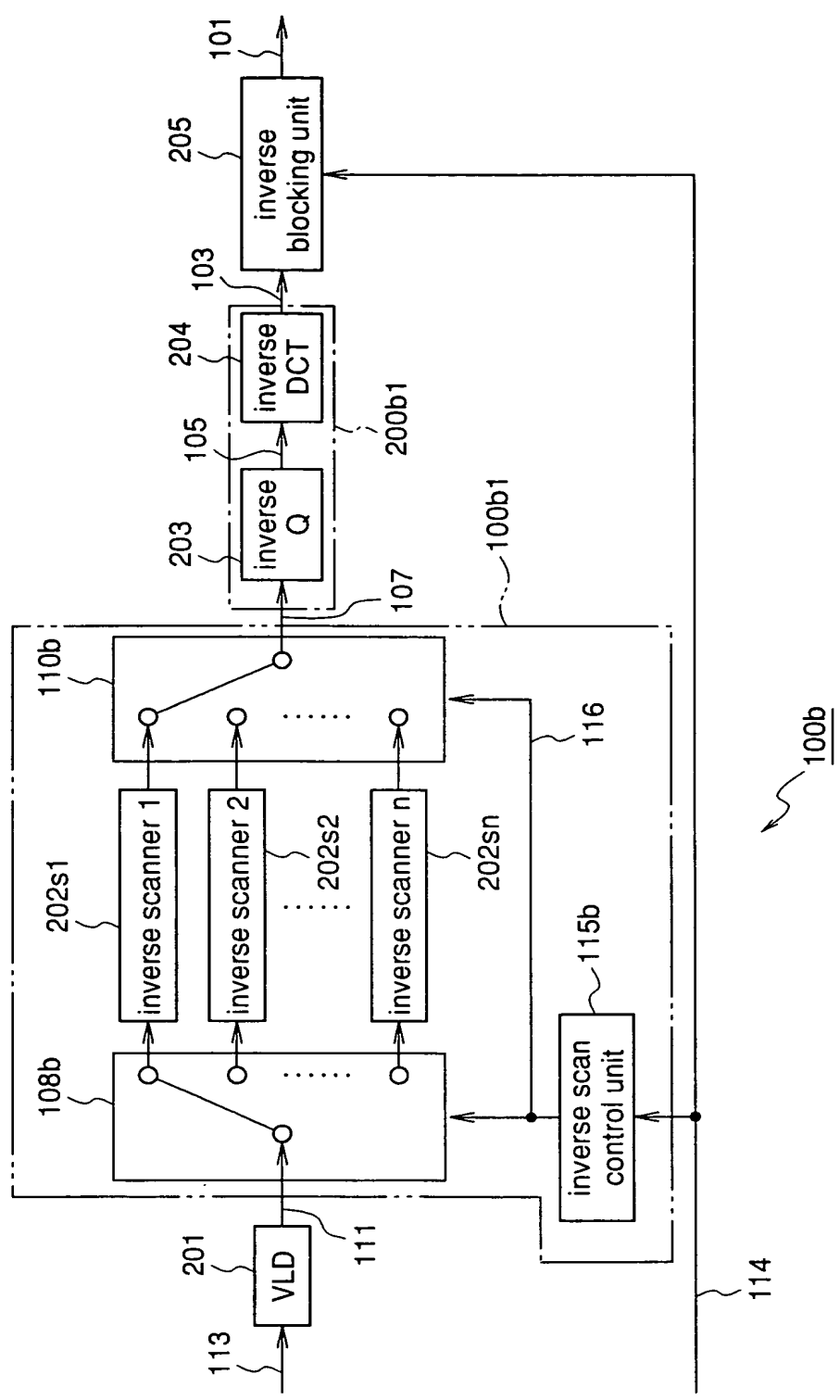


Fig.6



36340 T003T00

Fig.7

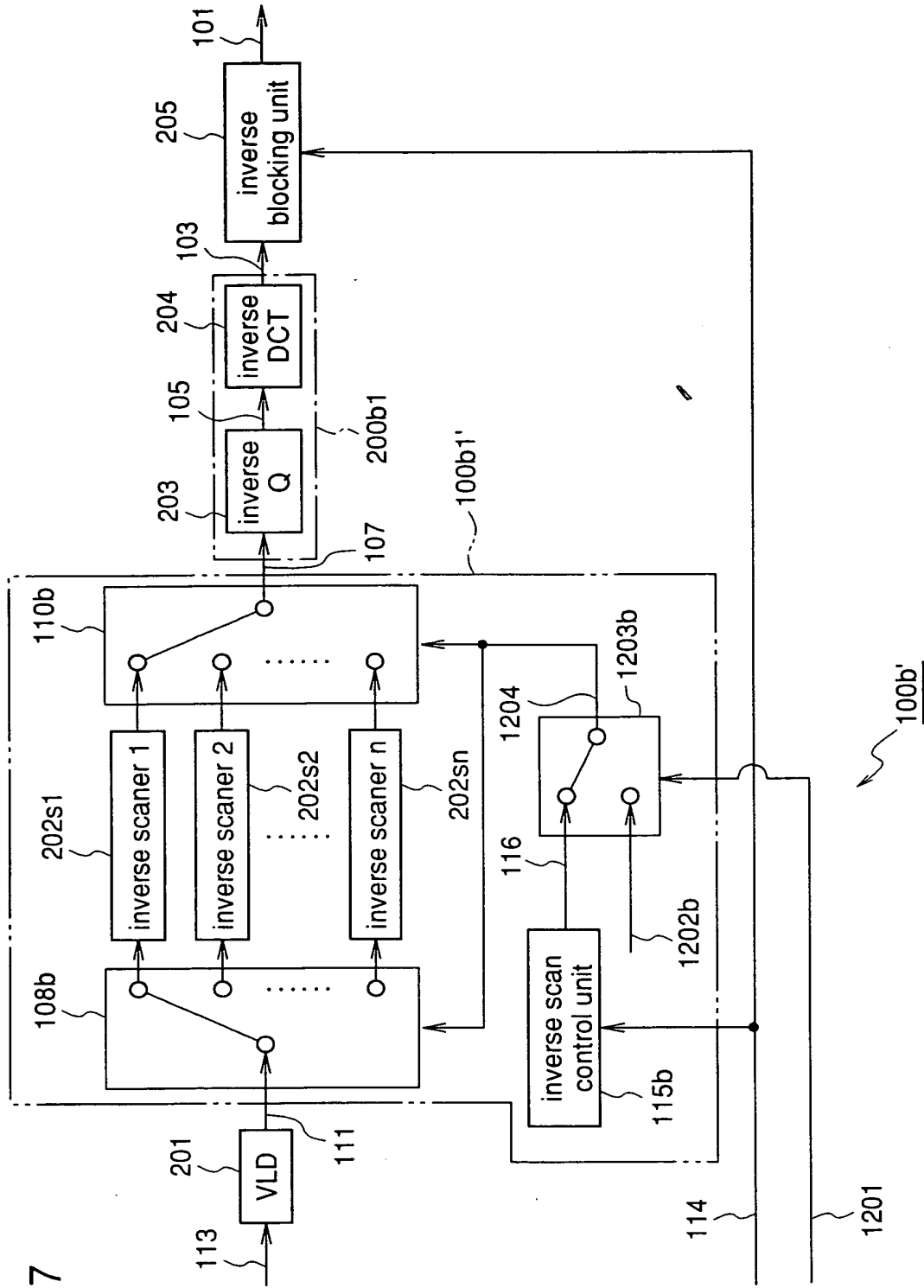
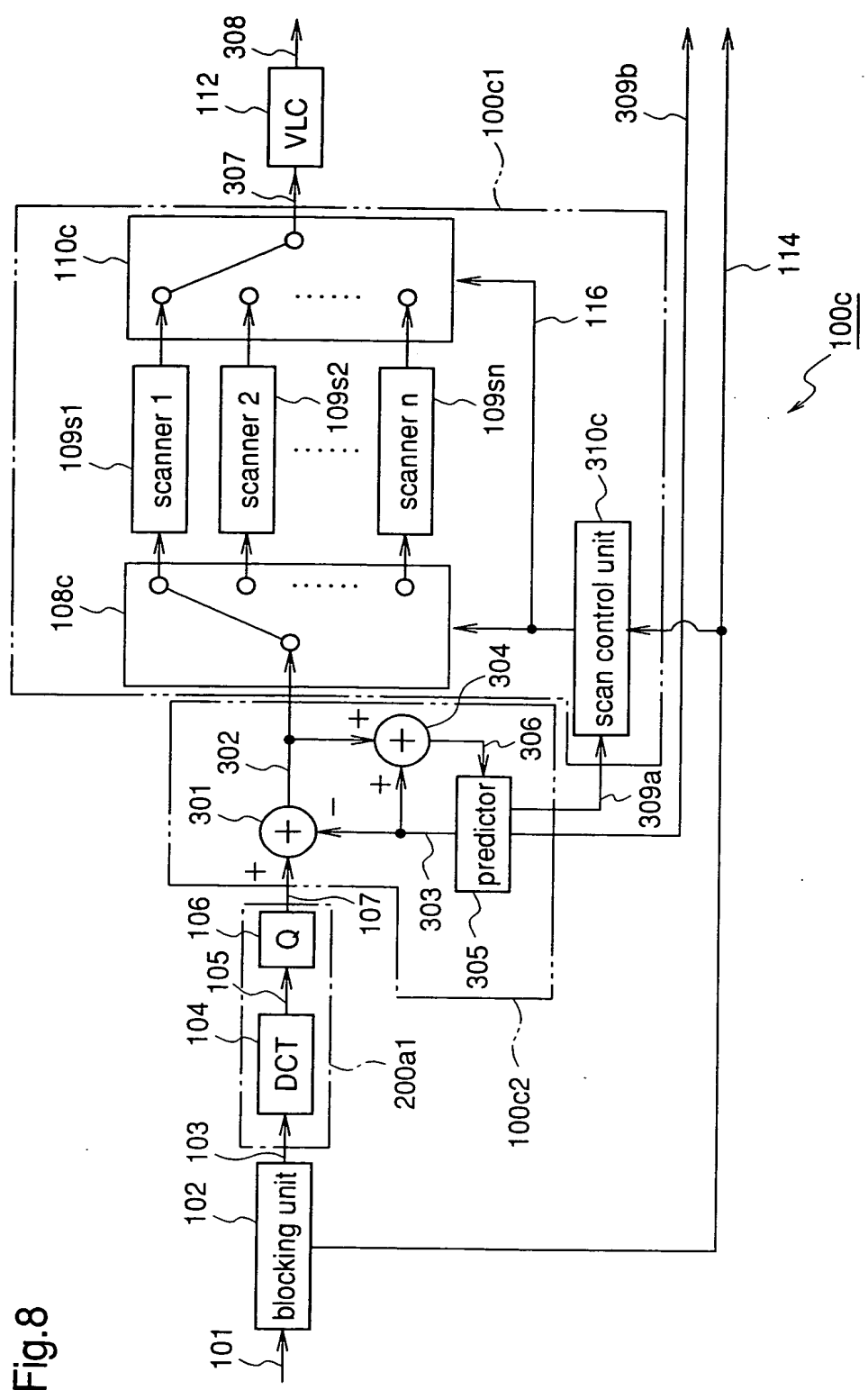


Fig.8



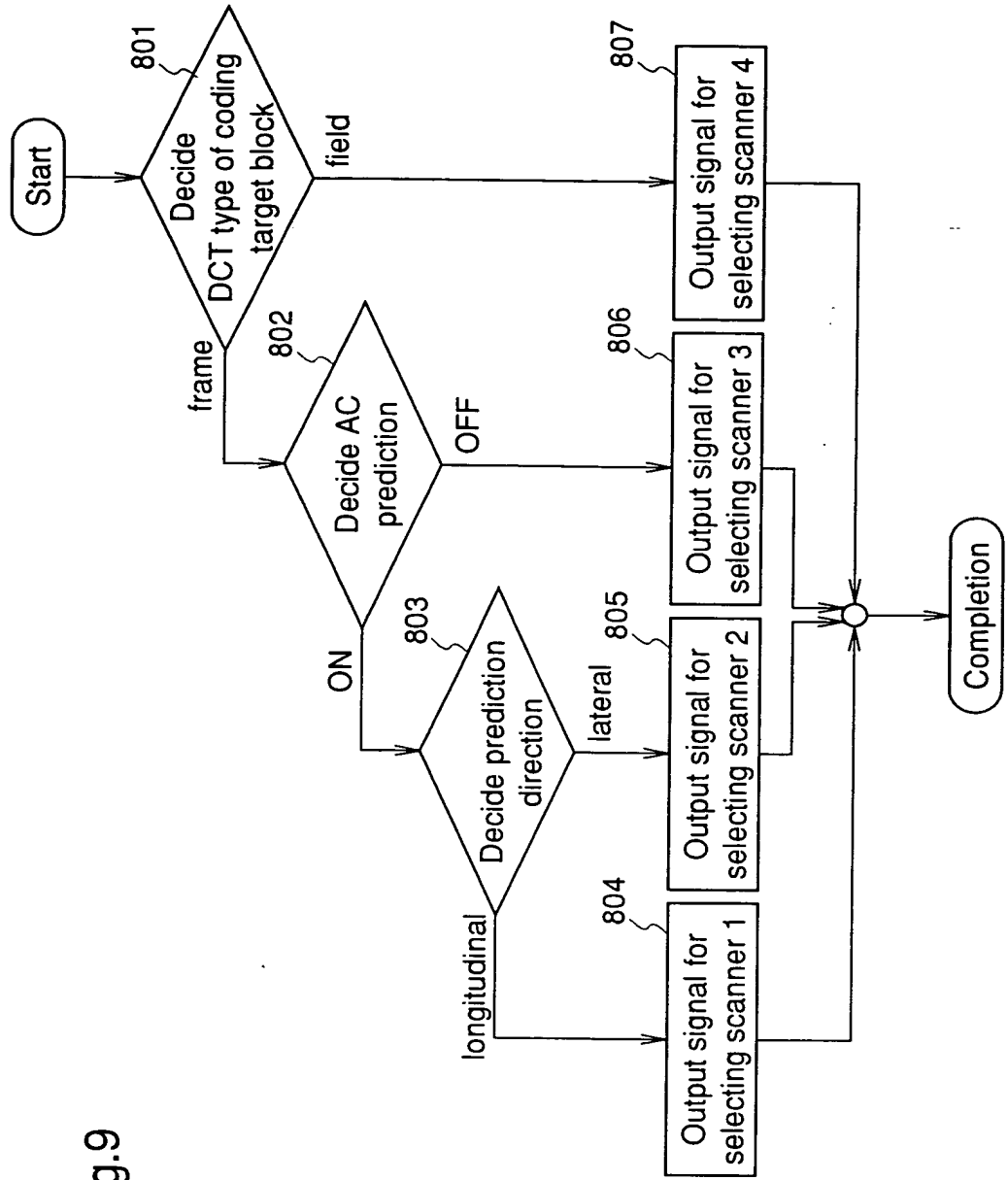


Fig.9

SU-660-165T60

SECRET

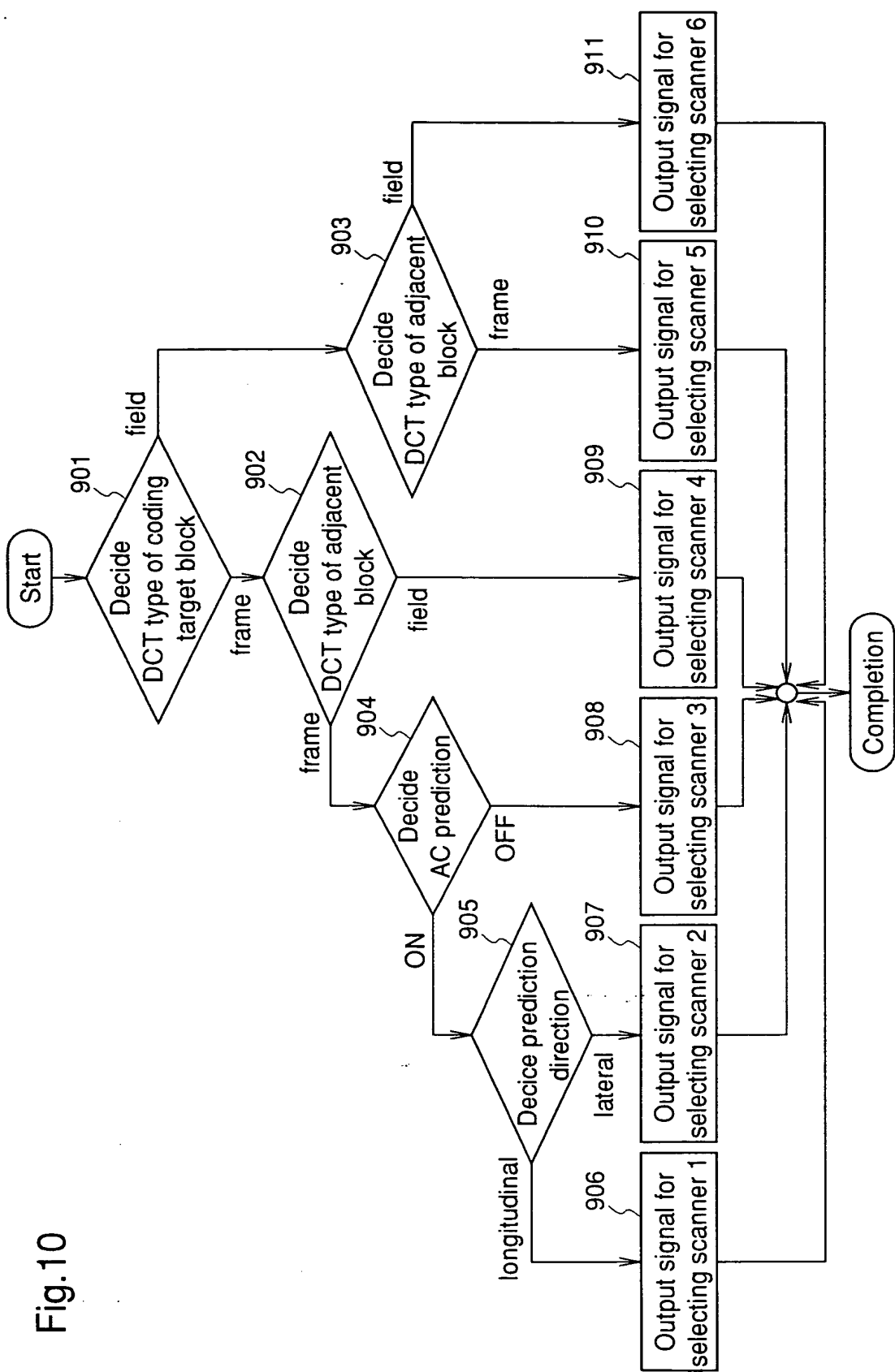


Fig.10

SECRET

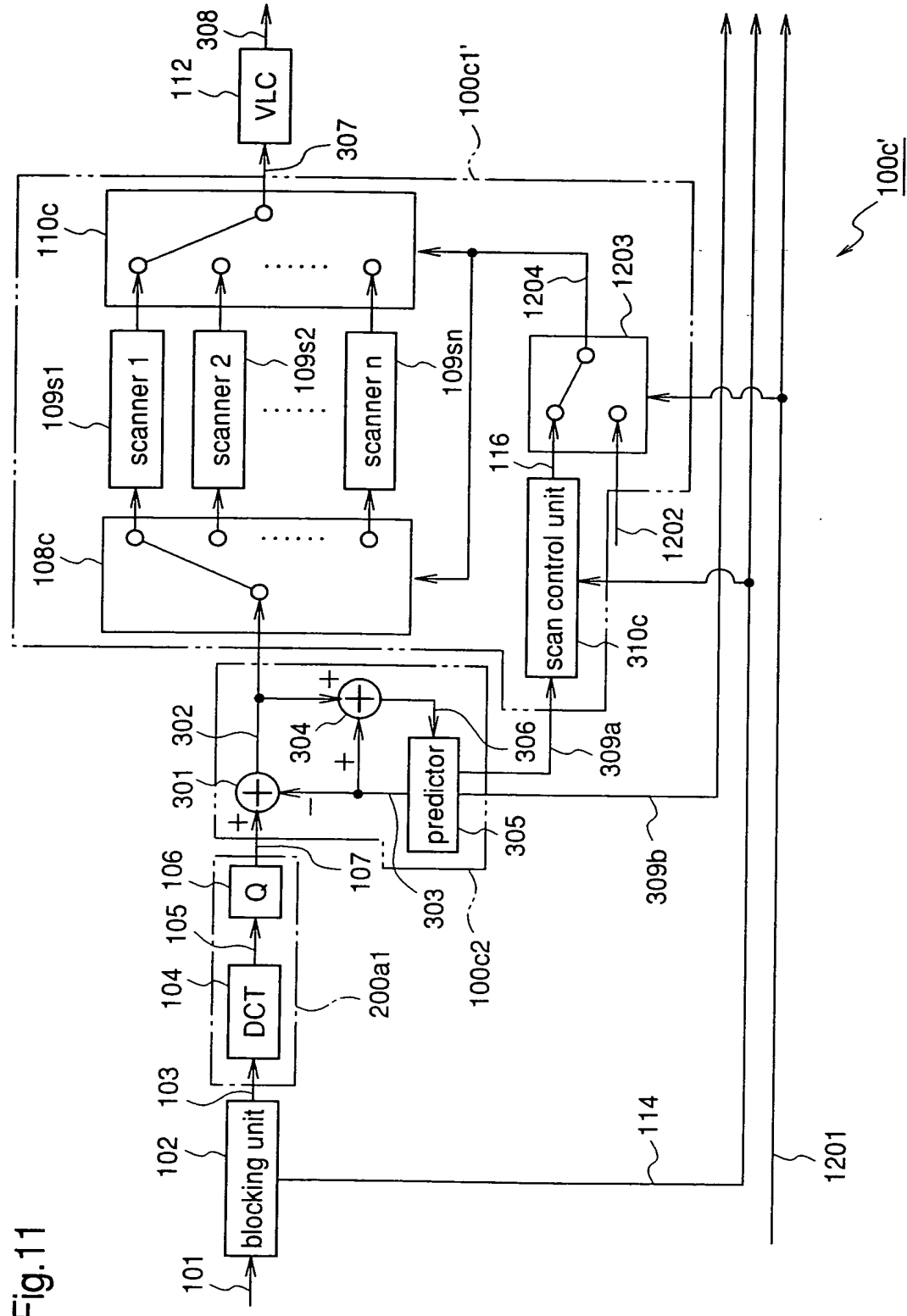


Fig. 11

Sheet 1 of 1

Fig.12

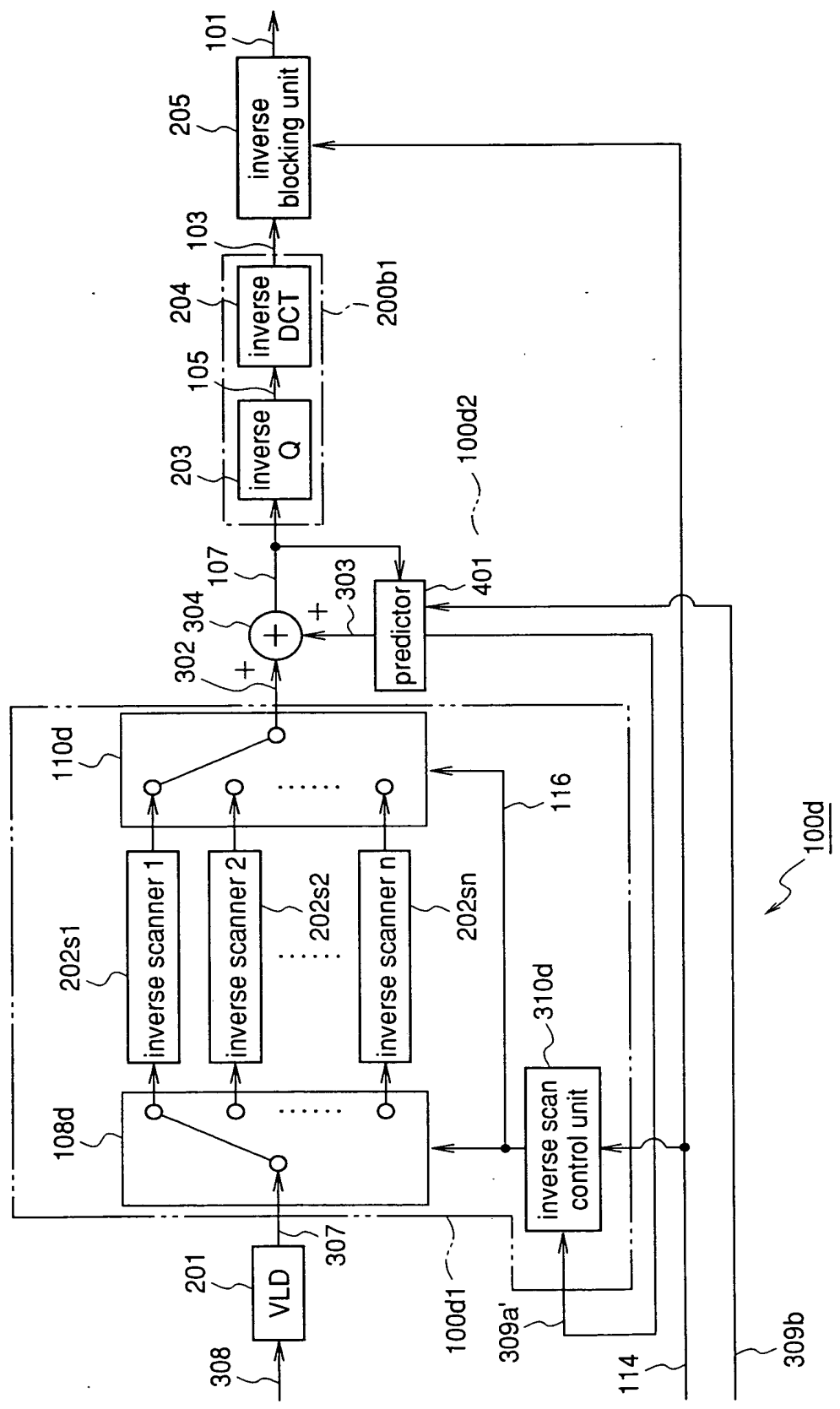
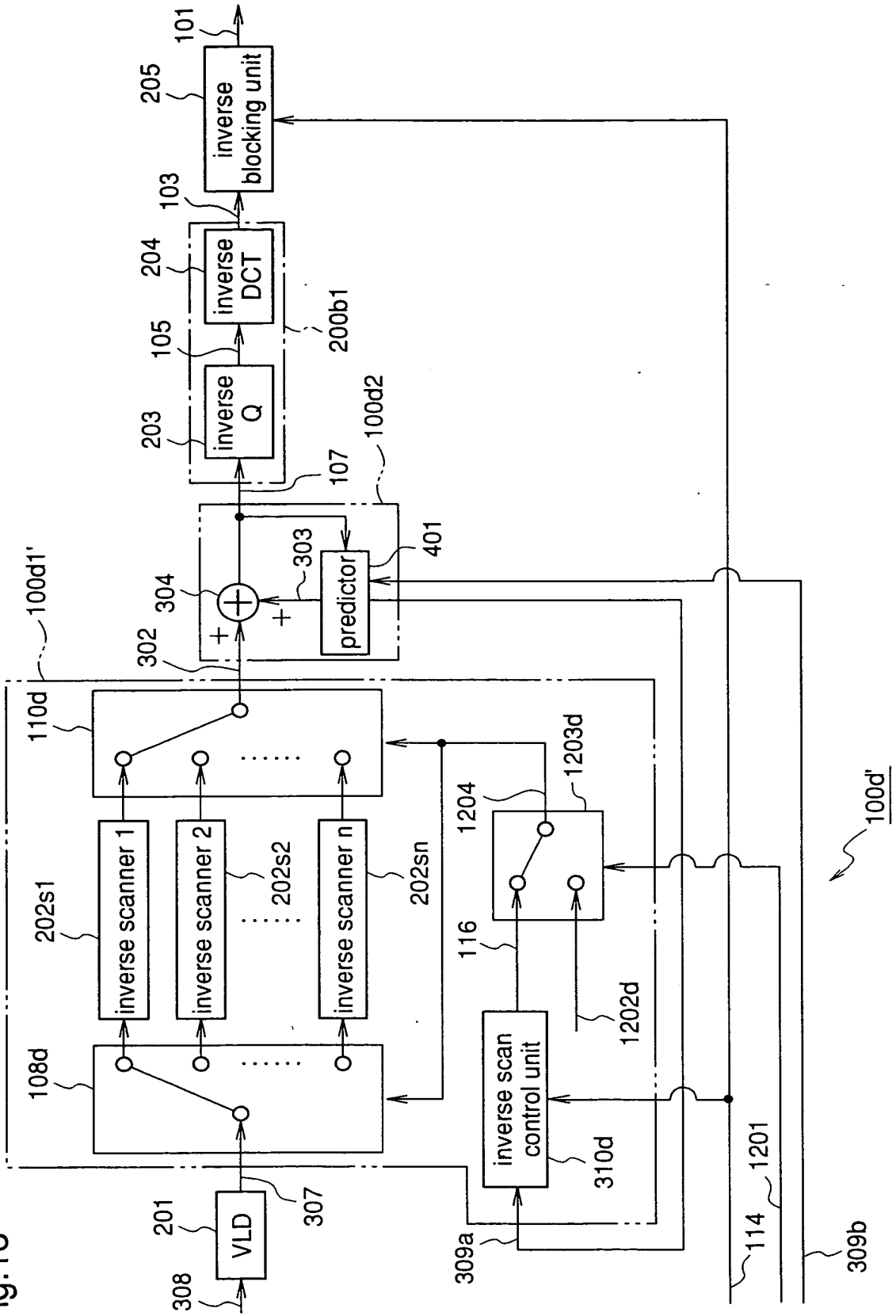


Fig.13



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APPROVED	O.G. FIG.	
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Fig.14

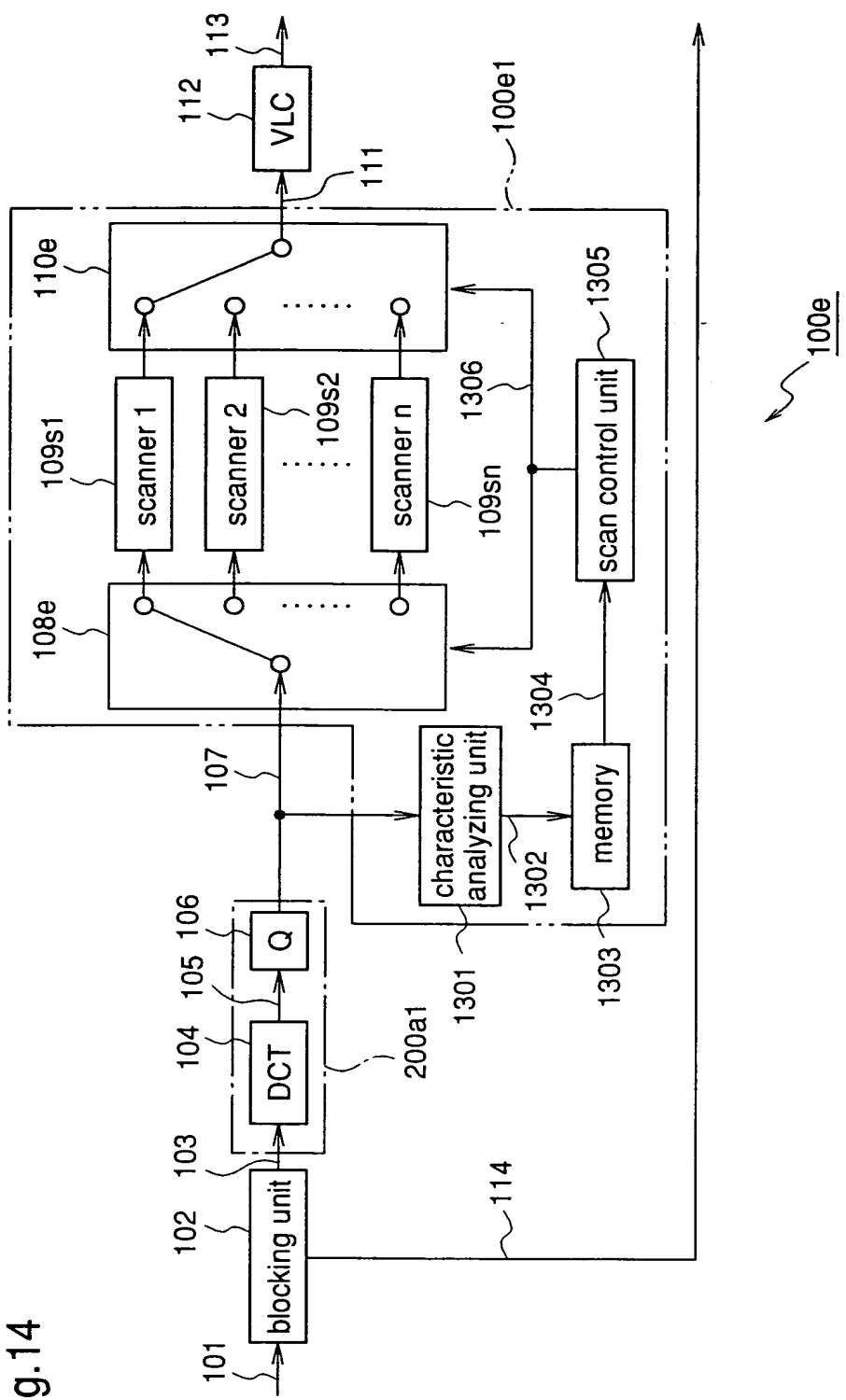


Fig.15

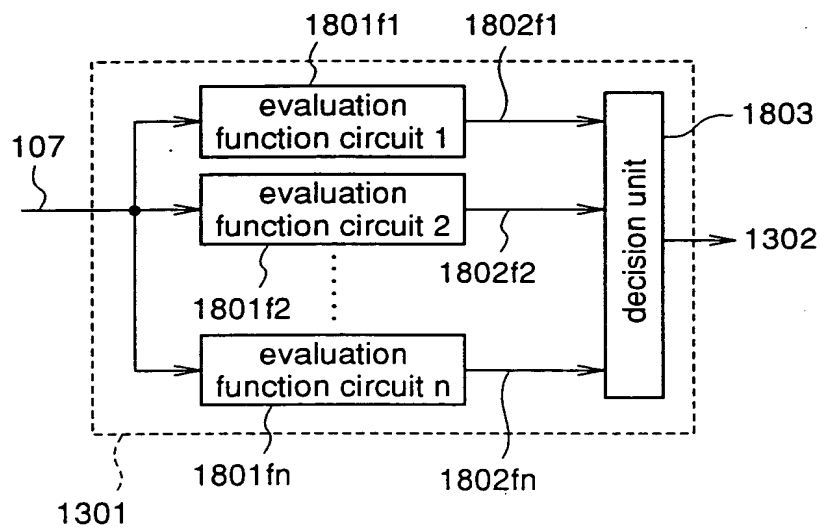
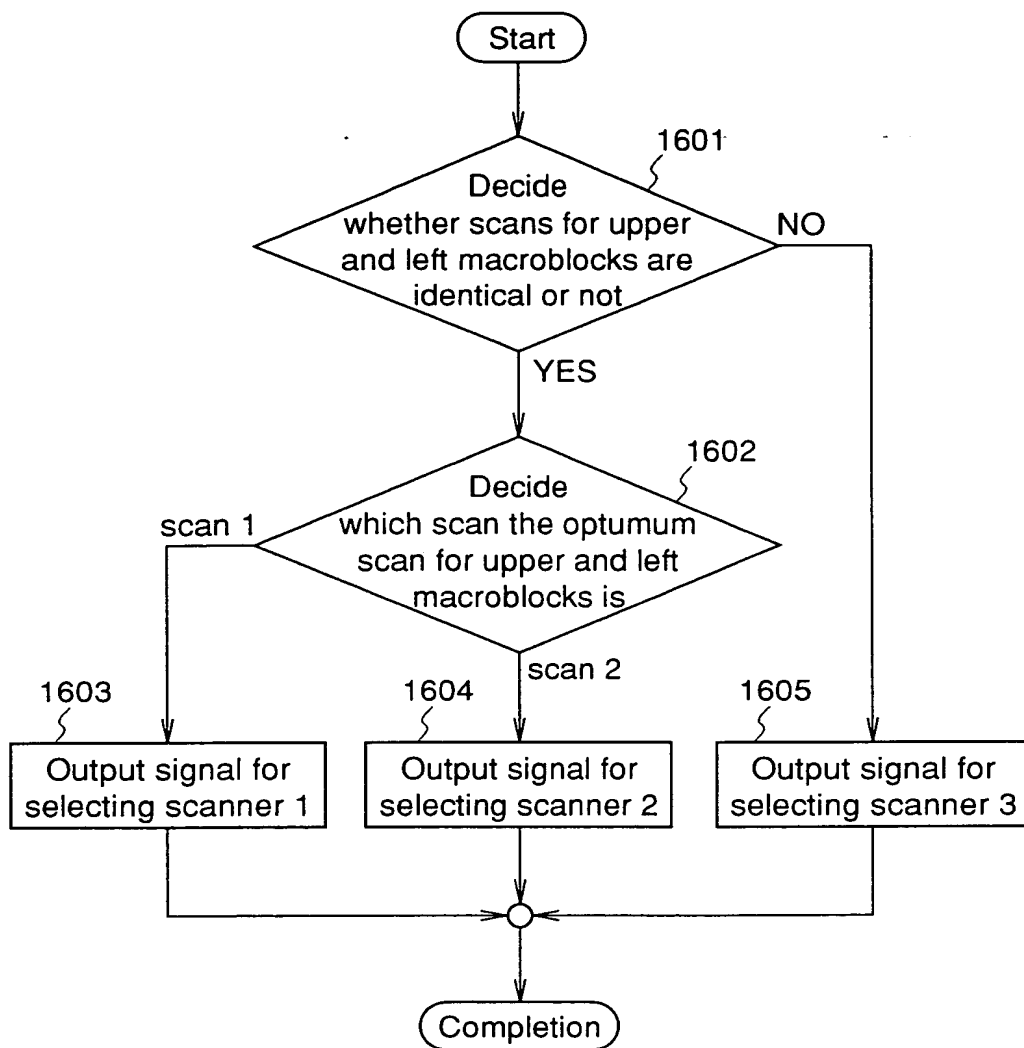


Fig.16



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Fig.18

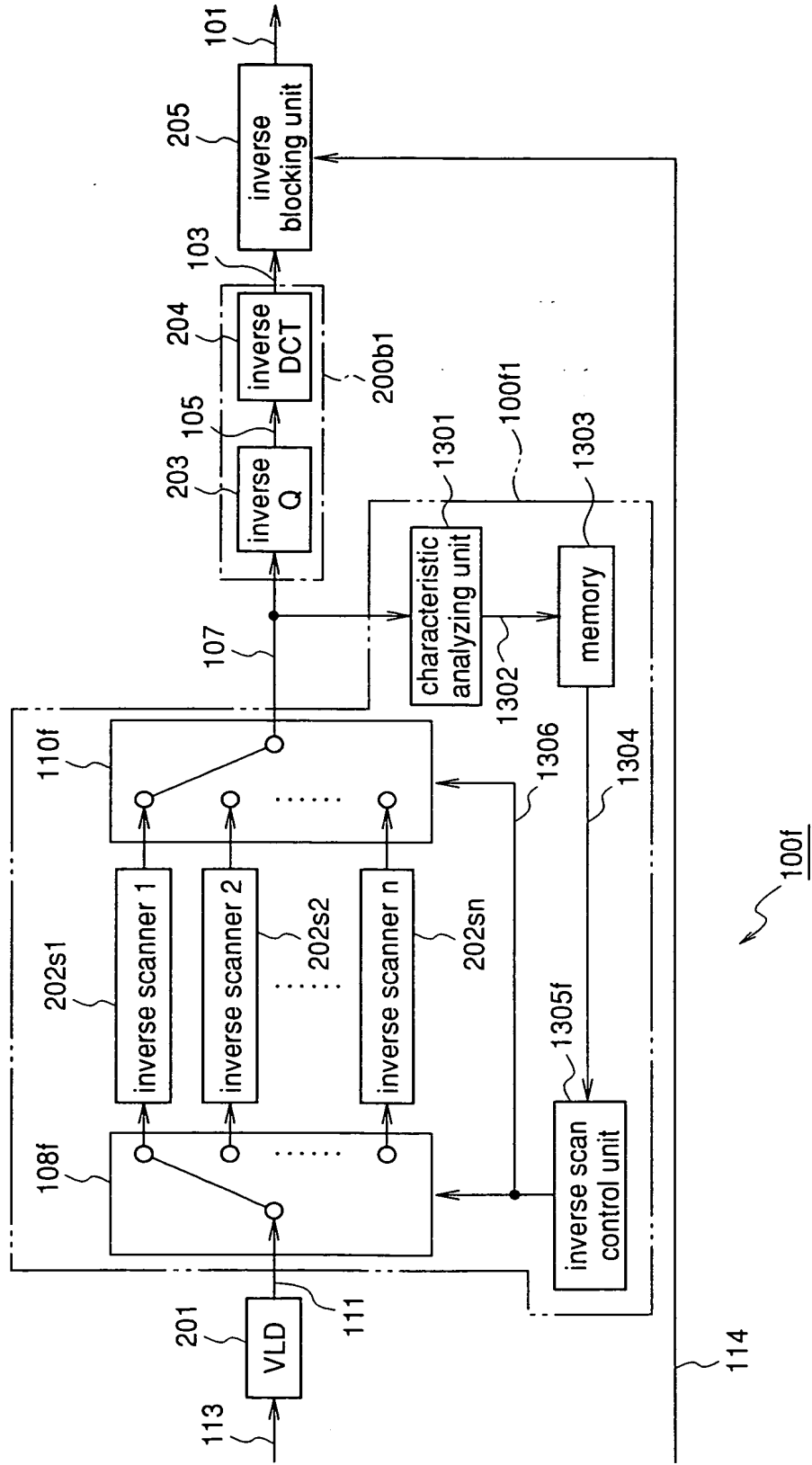
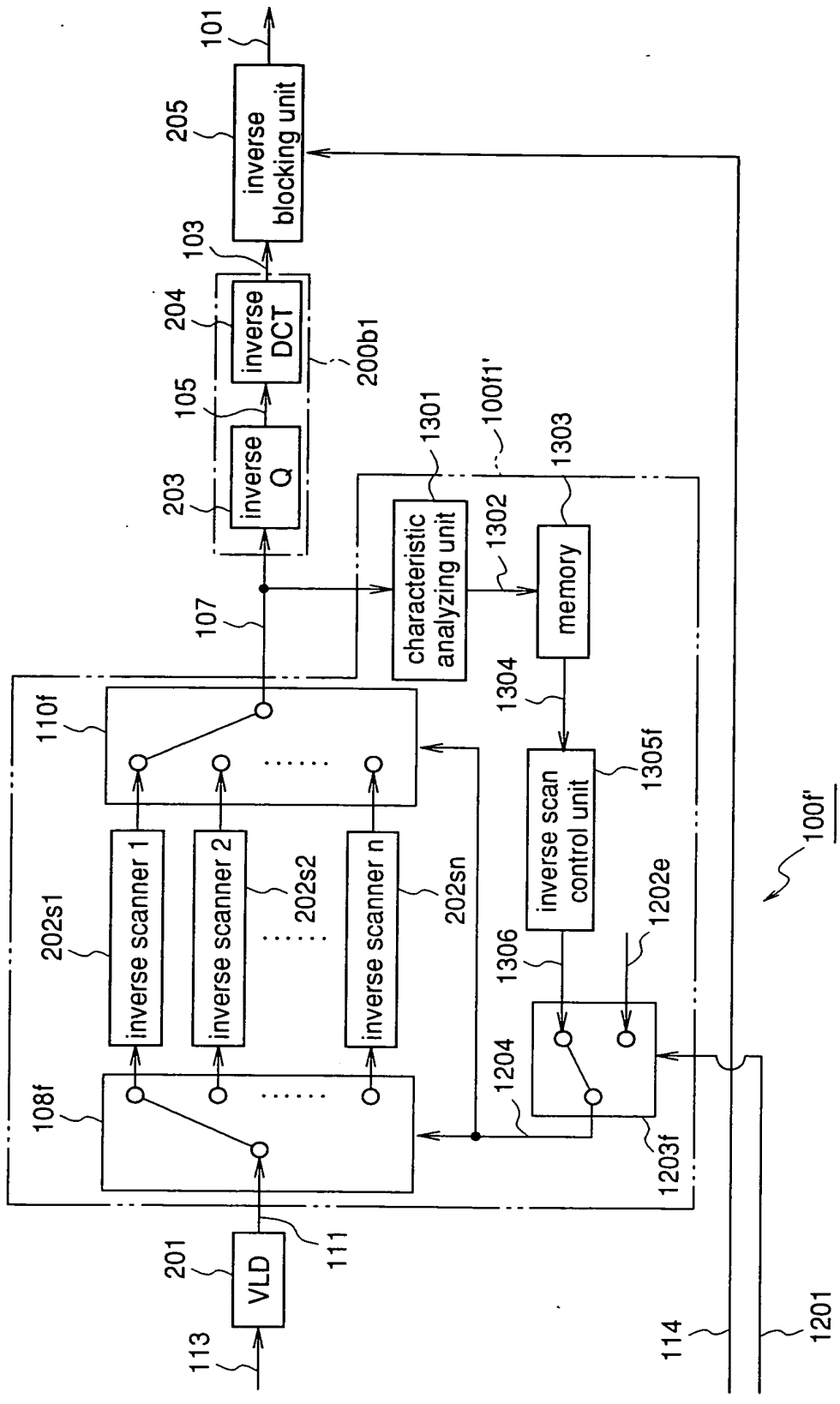


Fig.19



86220-163763

Fig.22

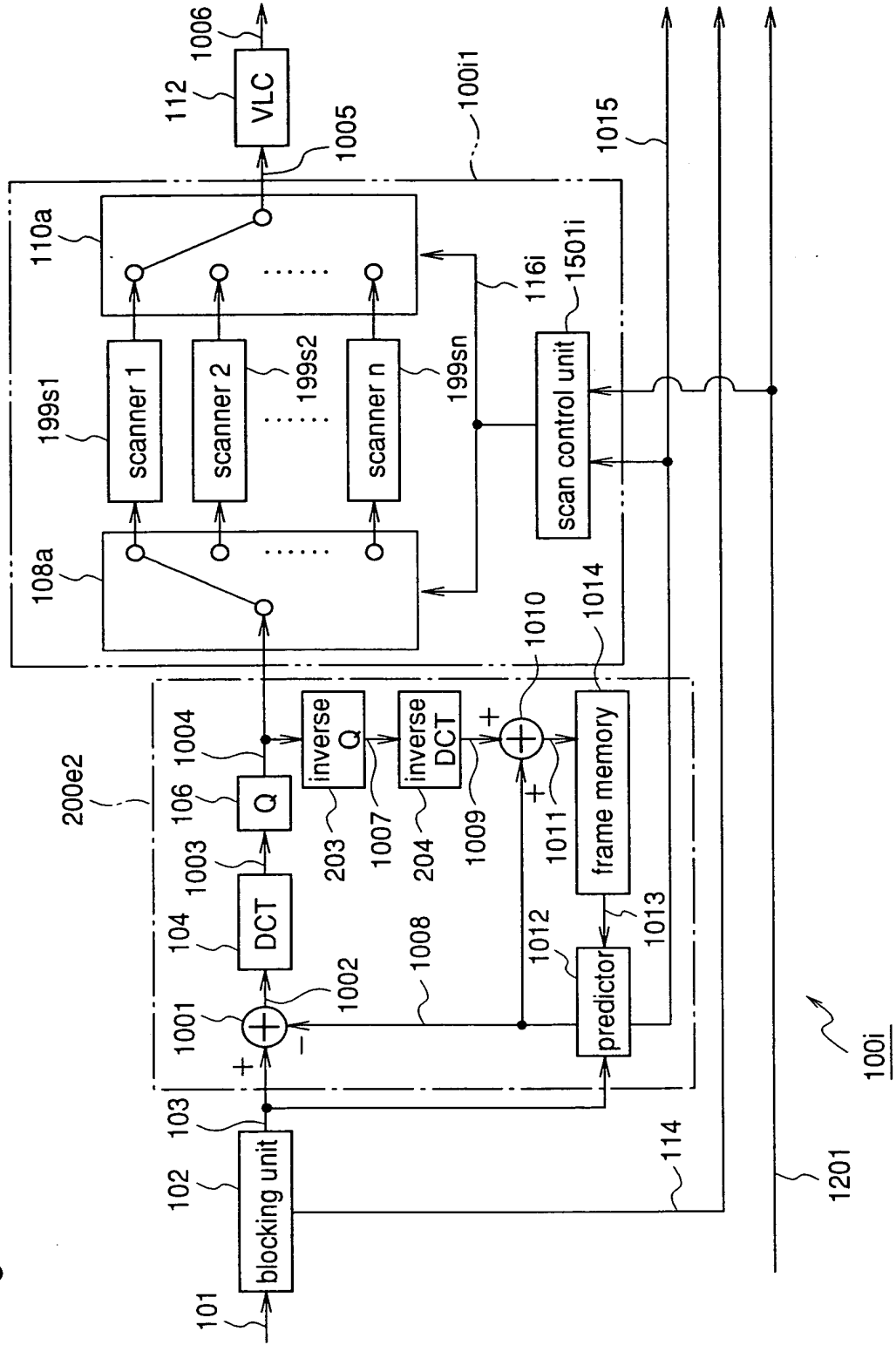


Fig.23

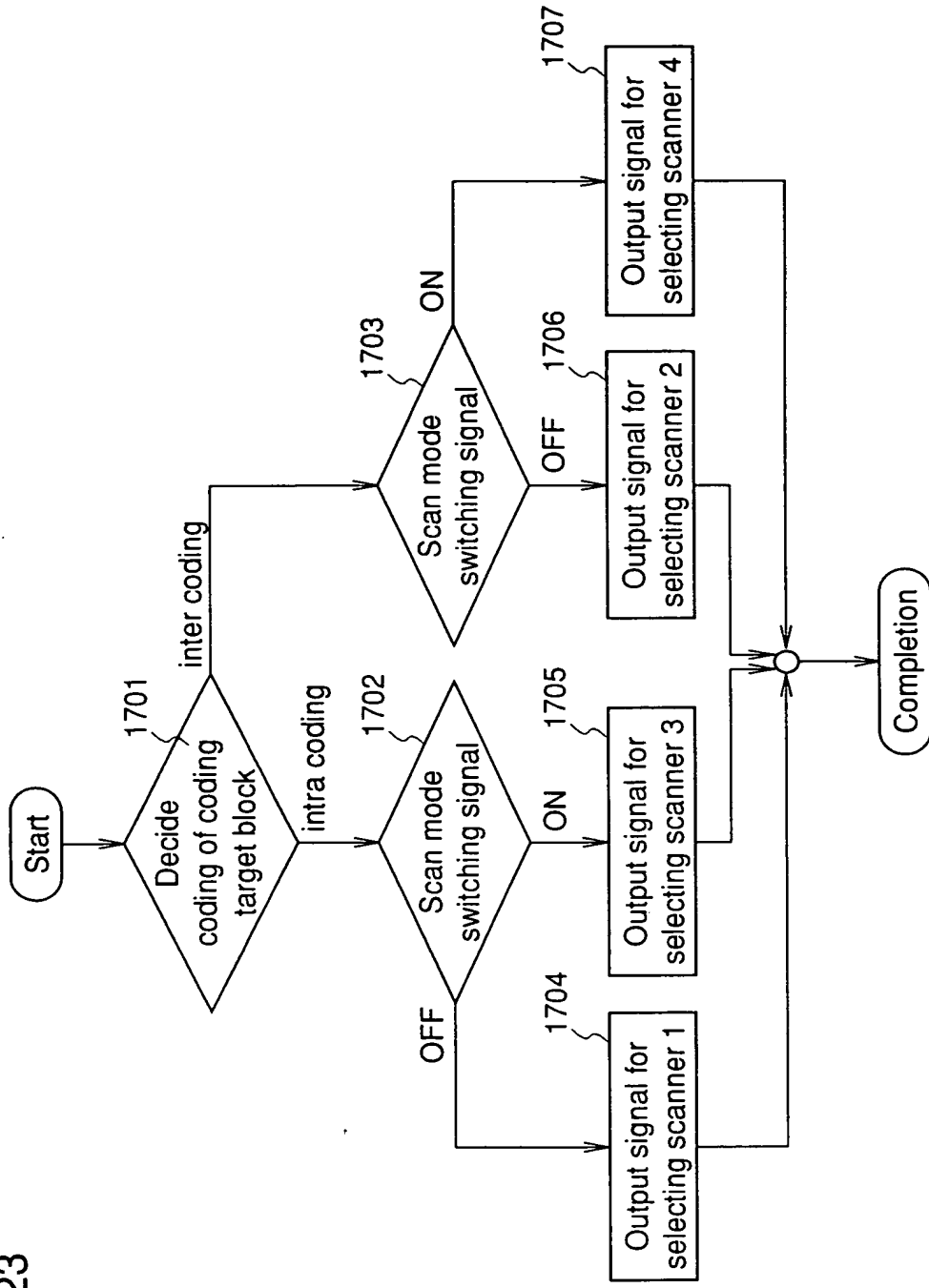


Fig.25

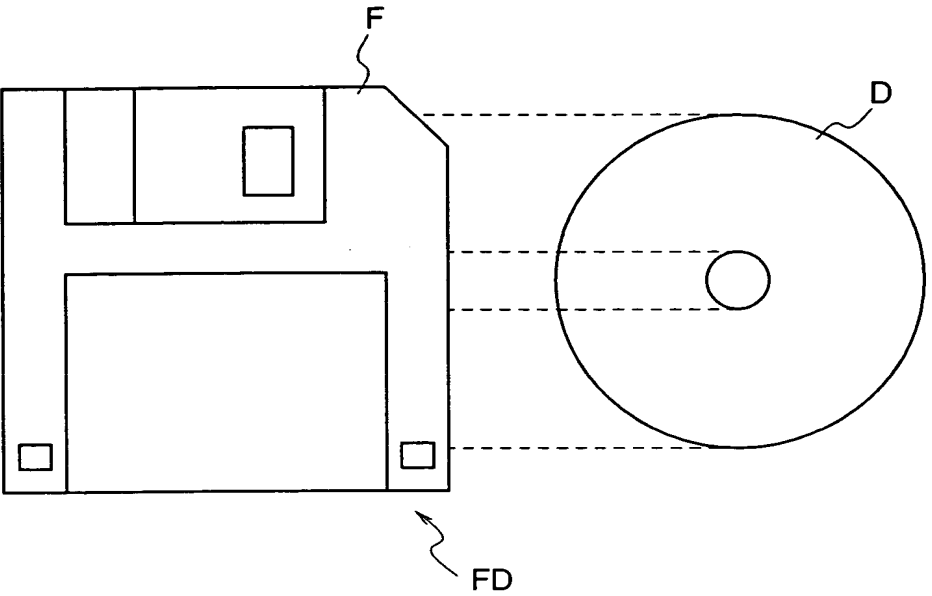
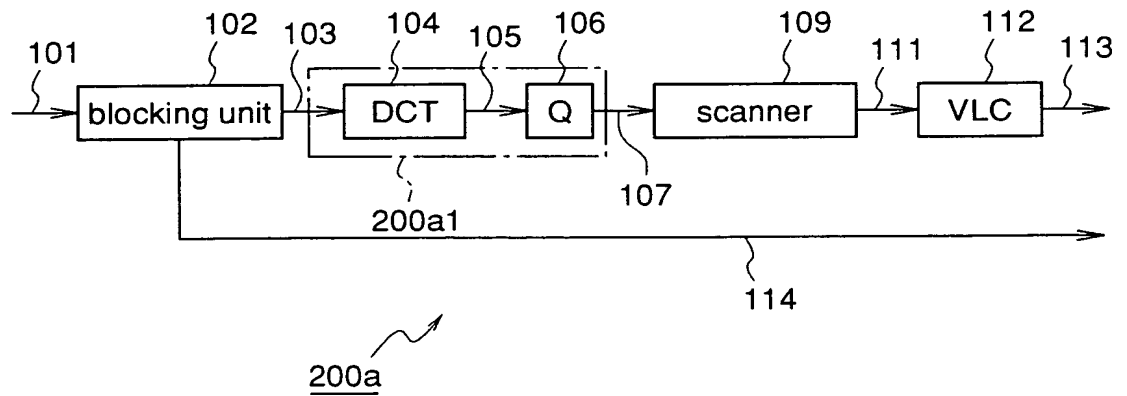
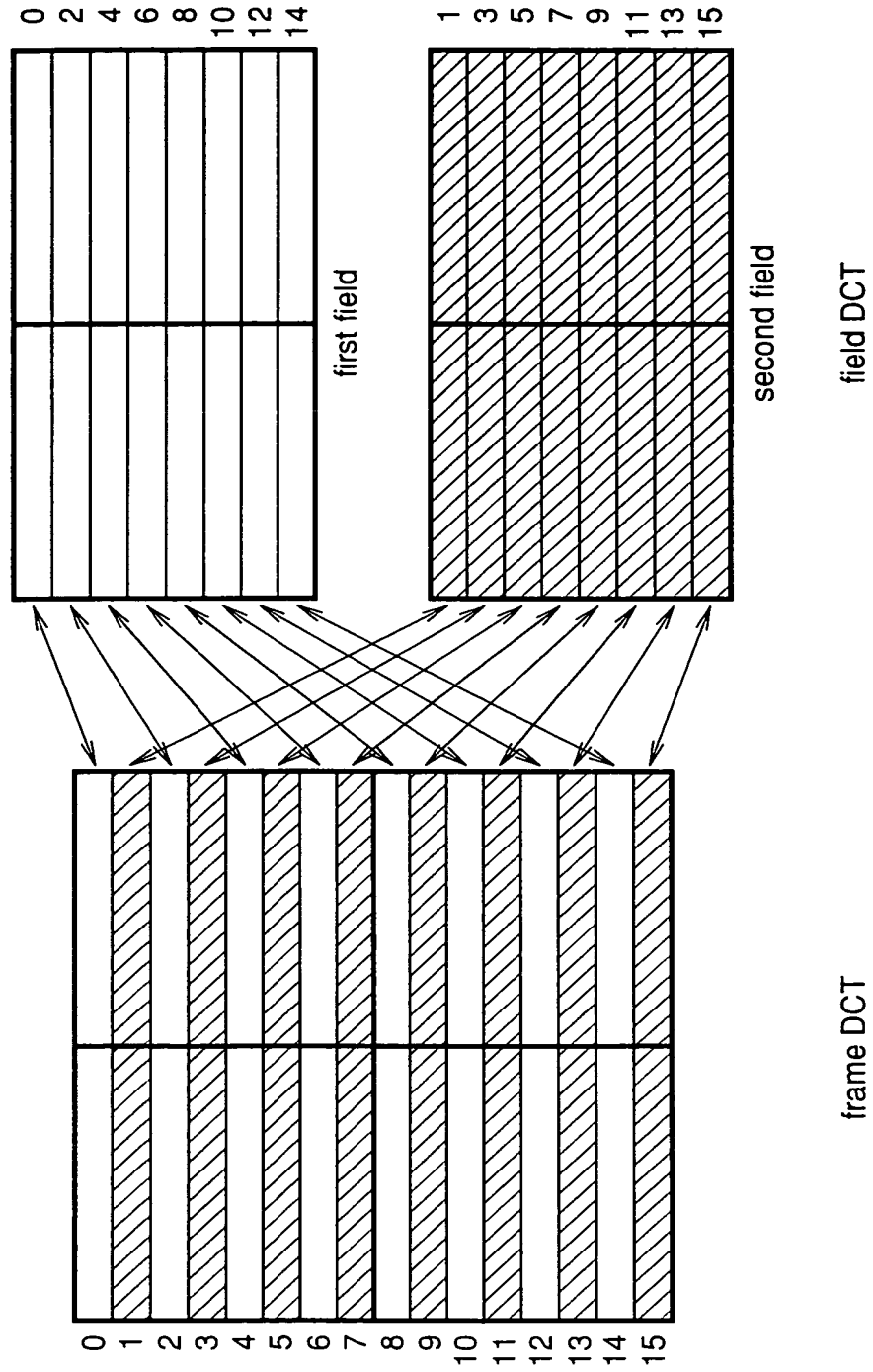


Fig.26 Prior Art



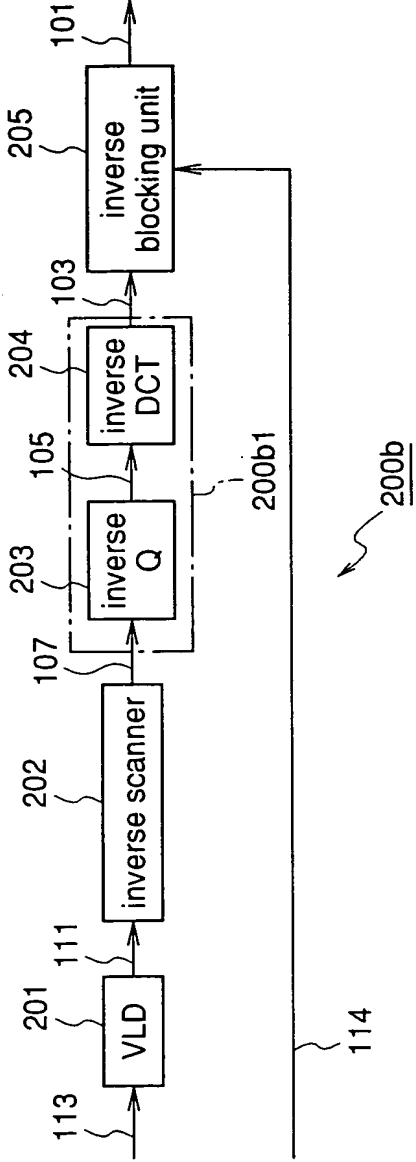
APPROVED	O.G. FIG.	
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Fig.27



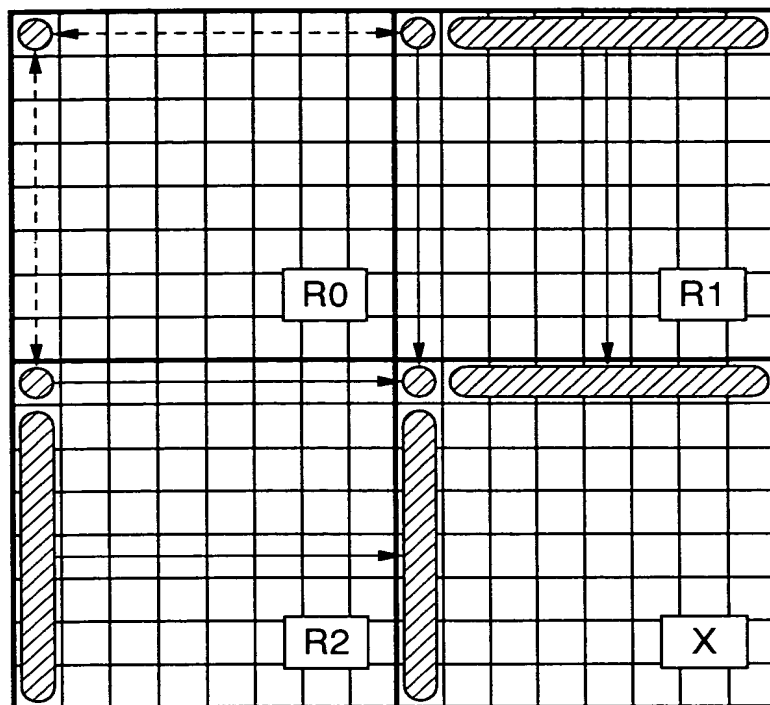
APPROVED	O.G. FIG.	
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Fig.28 Prior Art



APPROVED	O.G. FIG.	
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DRAFTSMAN		

Fig.30



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APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Fig.31 (a)

0	1	5	6	14	15	27	28
2	4	7	13	16	26	29	42
3	8	12	17	25	30	41	43
9	11	18	24	31	40	44	53
10	19	23	32	39	45	52	54
20	22	33	38	46	51	55	60
21	34	37	47	50	56	59	61
35	36	48	49	57	58	62	63

Fig.31 (b)

0	1	2	3	10	11	12	13
4	5	8	9	17	16	15	14
6	7	19	18	26	27	28	29
20	21	24	25	30	31	32	33
22	23	34	35	42	43	44	45
36	37	40	41	46	47	48	49
38	39	50	51	56	57	58	59
52	53	54	55	60	61	62	63

Fig.31 (c)

0	4	6	20	22	36	38	52
1	5	7	21	23	37	39	53
2	8	19	24	34	40	50	54
3	9	18	25	35	41	51	55
10	17	26	30	42	46	56	60
11	16	27	31	43	47	57	61
12	15	28	32	44	48	58	62
13	14	29	33	45	49	59	63

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Fig.32 Prior Art

The diagram illustrates a video decoding system (Fig. 32) with the following components and signal flow:

- Input 308** feeds into the **VLD (201)** block.
- The output of VLD (201) goes to the **inverse scan control unit (1401d)**.
- The **inverse scan control unit (1401d)** outputs **309a'** and **309b** to the **inverse scanner 1 (202s1)**, **inverse scanner 2 (202s2)**, and **inverse scanner 3 (202s3)**.
- The outputs of the three inverse scanners are combined in a summing junction (302) to produce **304**.
- The output of the summing junction (304) is added to the output of the **predictor (401)** (labeled **303**) to produce **107d**.
- The output of the summing junction (304) also passes through an **inverse Q (203)** block and an **inverse DCT (204)** block.
- The output of the inverse DCT (204) is added to the output of the inverse Q (203) block to produce **103**.
- The output of the inverse DCT (204) also passes through an **inverse blocking unit (205)** to produce the final output **101**.
- The output of the inverse blocking unit (205) is fed back to the **predictor (401)** via a feedback loop.
- The output of the inverse blocking unit (205) is also fed back to the **inverse scan control unit (1401d)** via a feedback loop.
- The output of the inverse scan control unit (1401d) is also fed back to the **inverse scanner 1 (202s1)** via a feedback loop.
- The output of the inverse scan control unit (1401d) is also fed back to the **inverse scanner 2 (202s2)** via a feedback loop.
- The output of the inverse scan control unit (1401d) is also fed back to the **inverse scanner 3 (202s3)** via a feedback loop.
- The output of the inverse scan control unit (1401d) is also fed back to the **inverse scan control unit (1401d)** via a feedback loop.

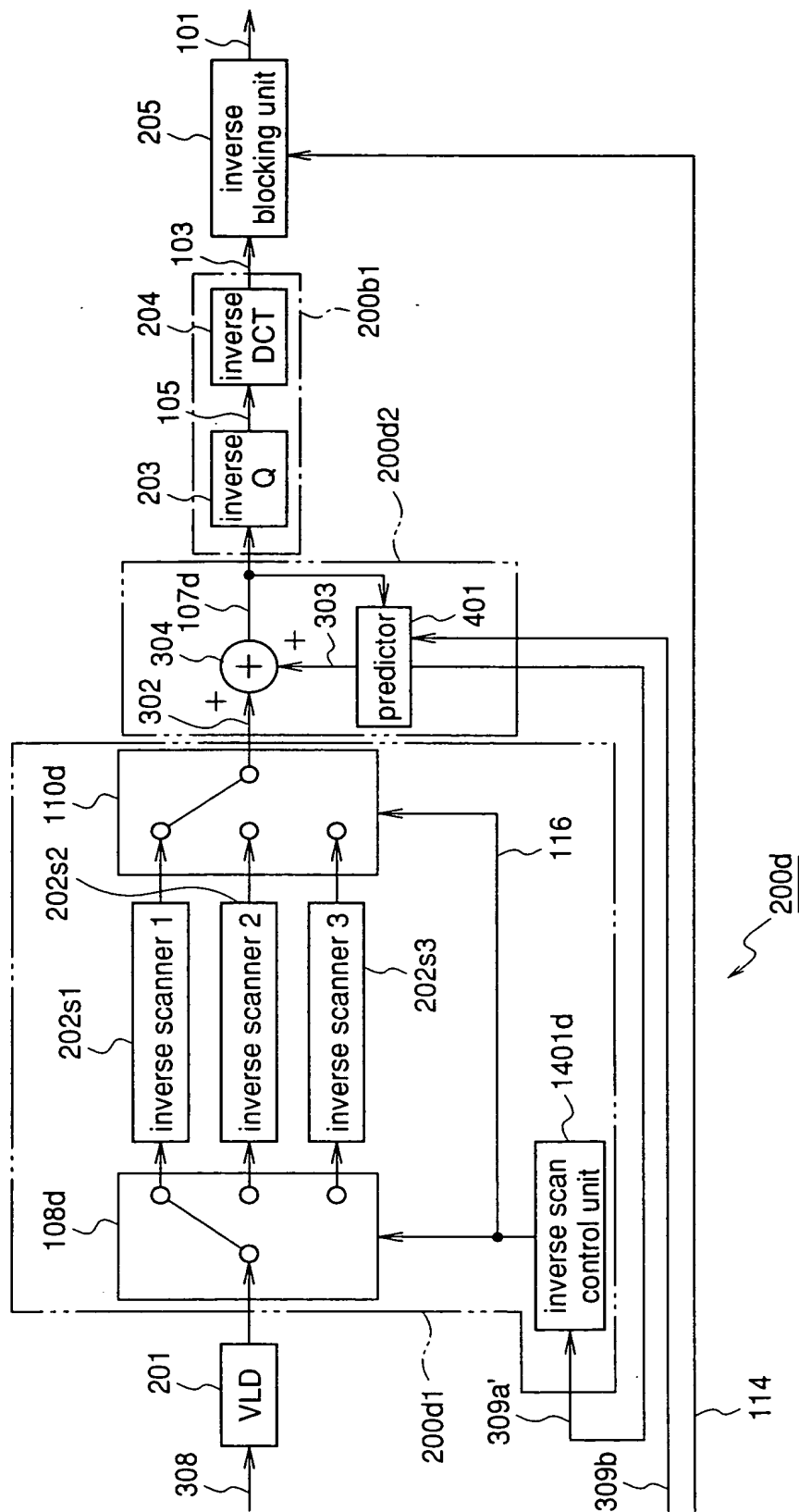


Fig.34 Prior Art

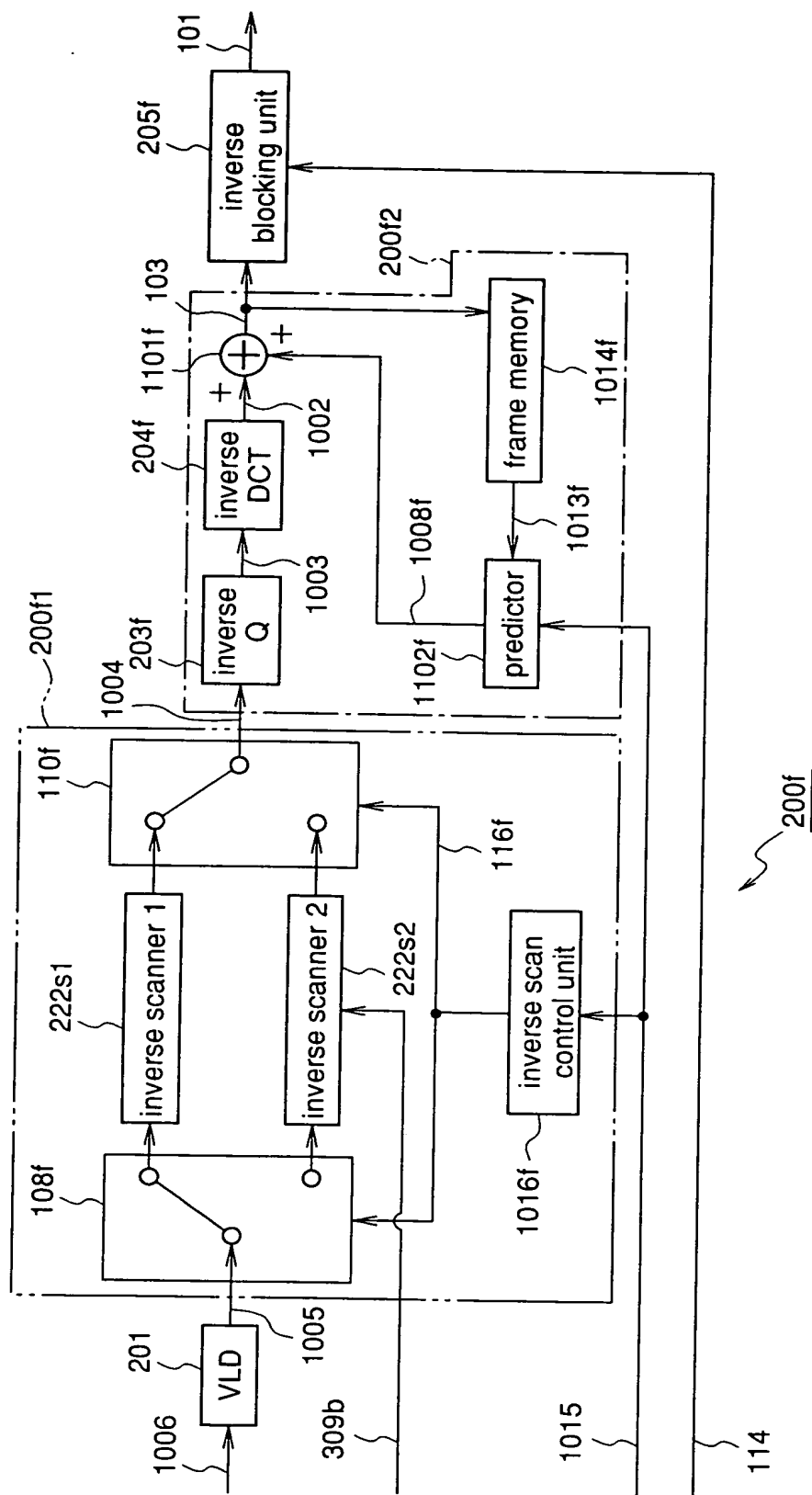


Fig.35

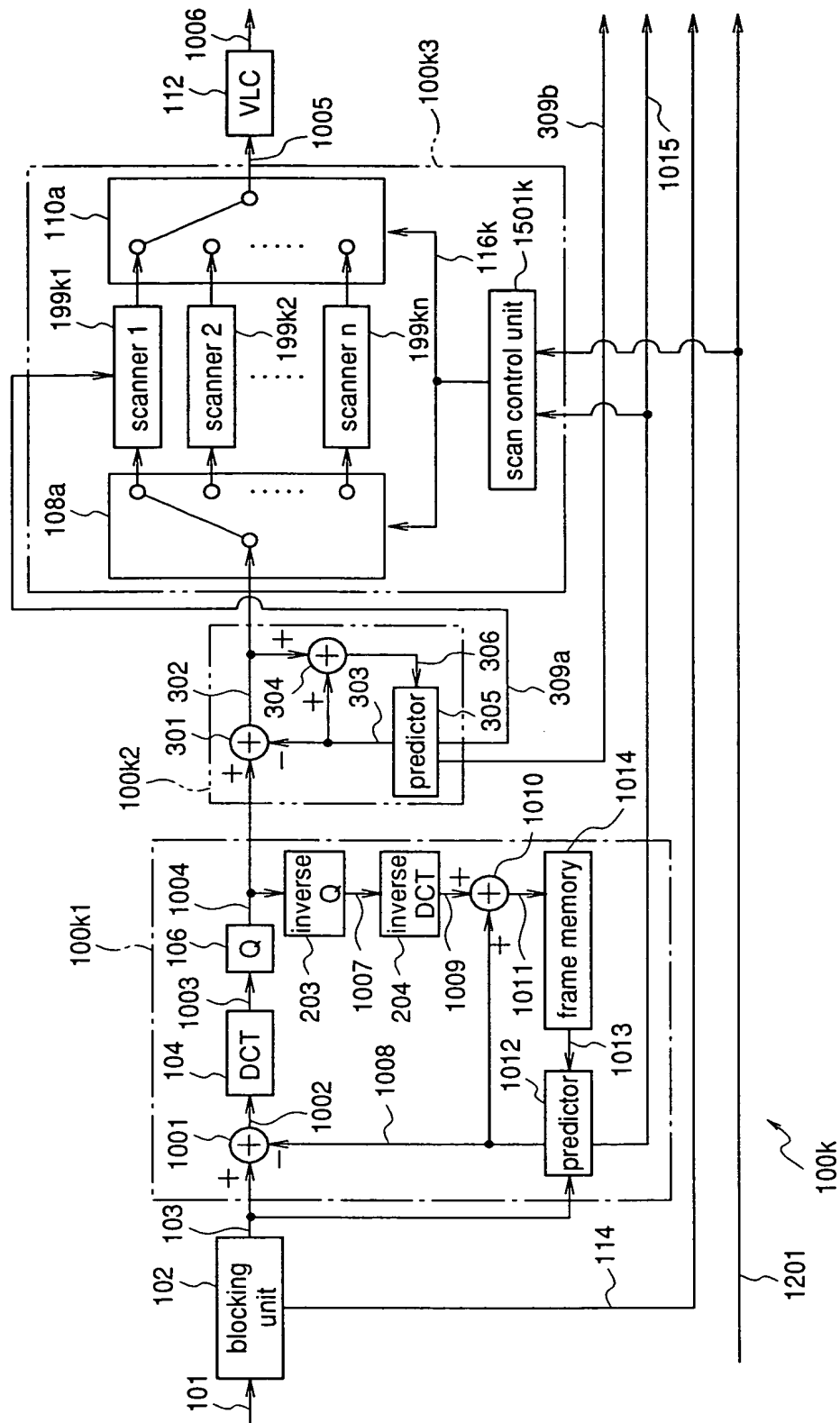
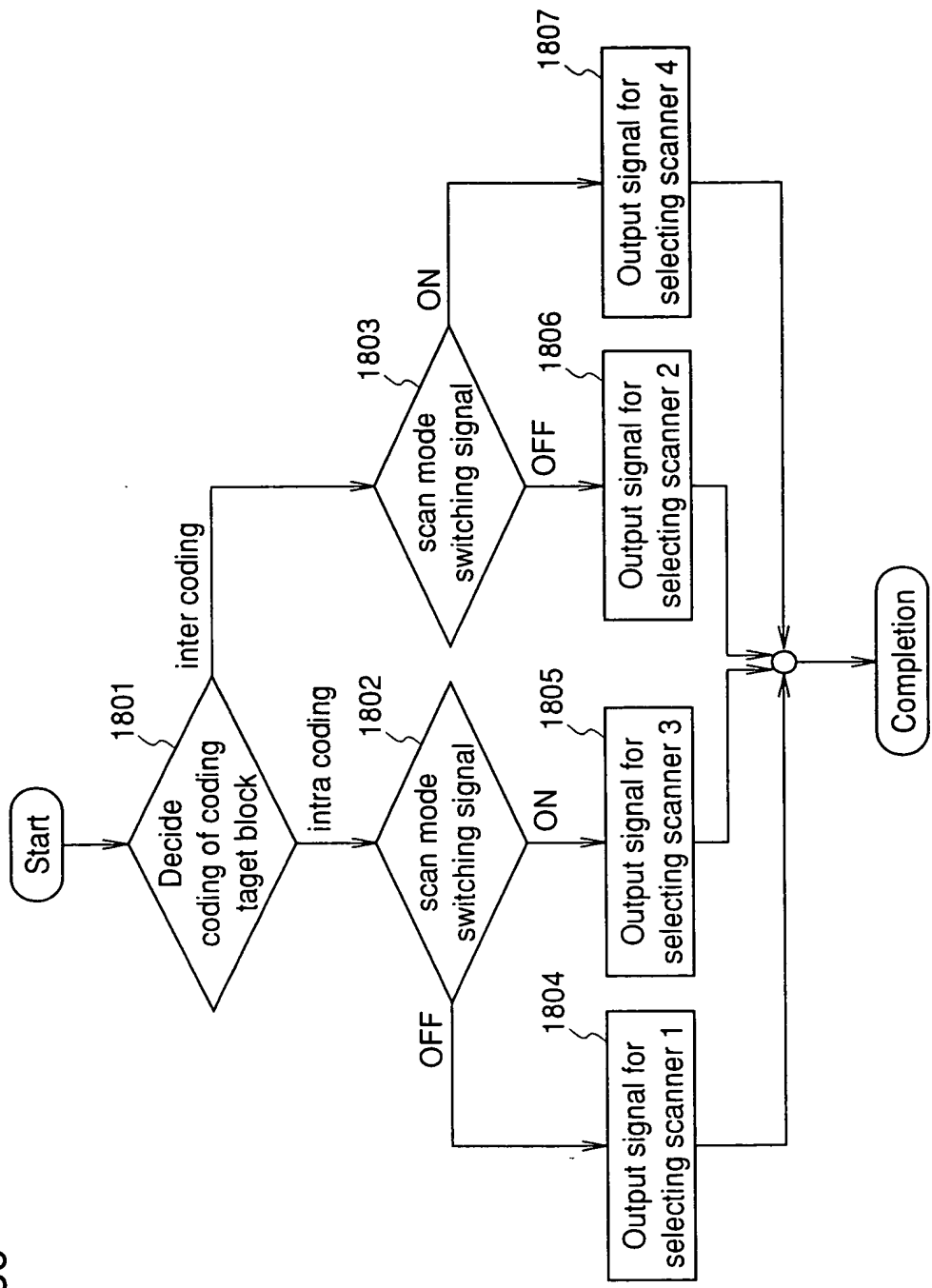
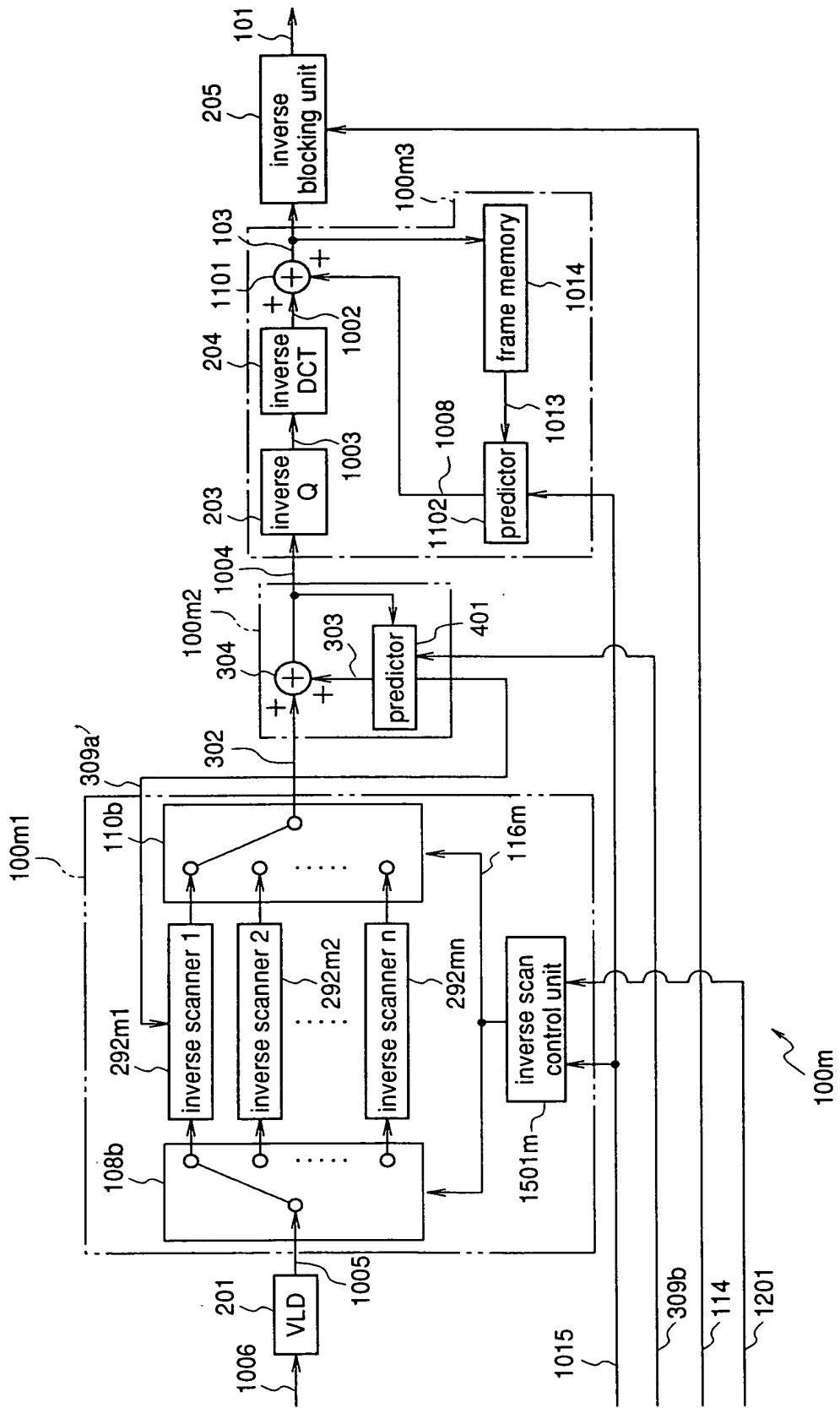


Fig.36



360220 T663T60

Fig.37



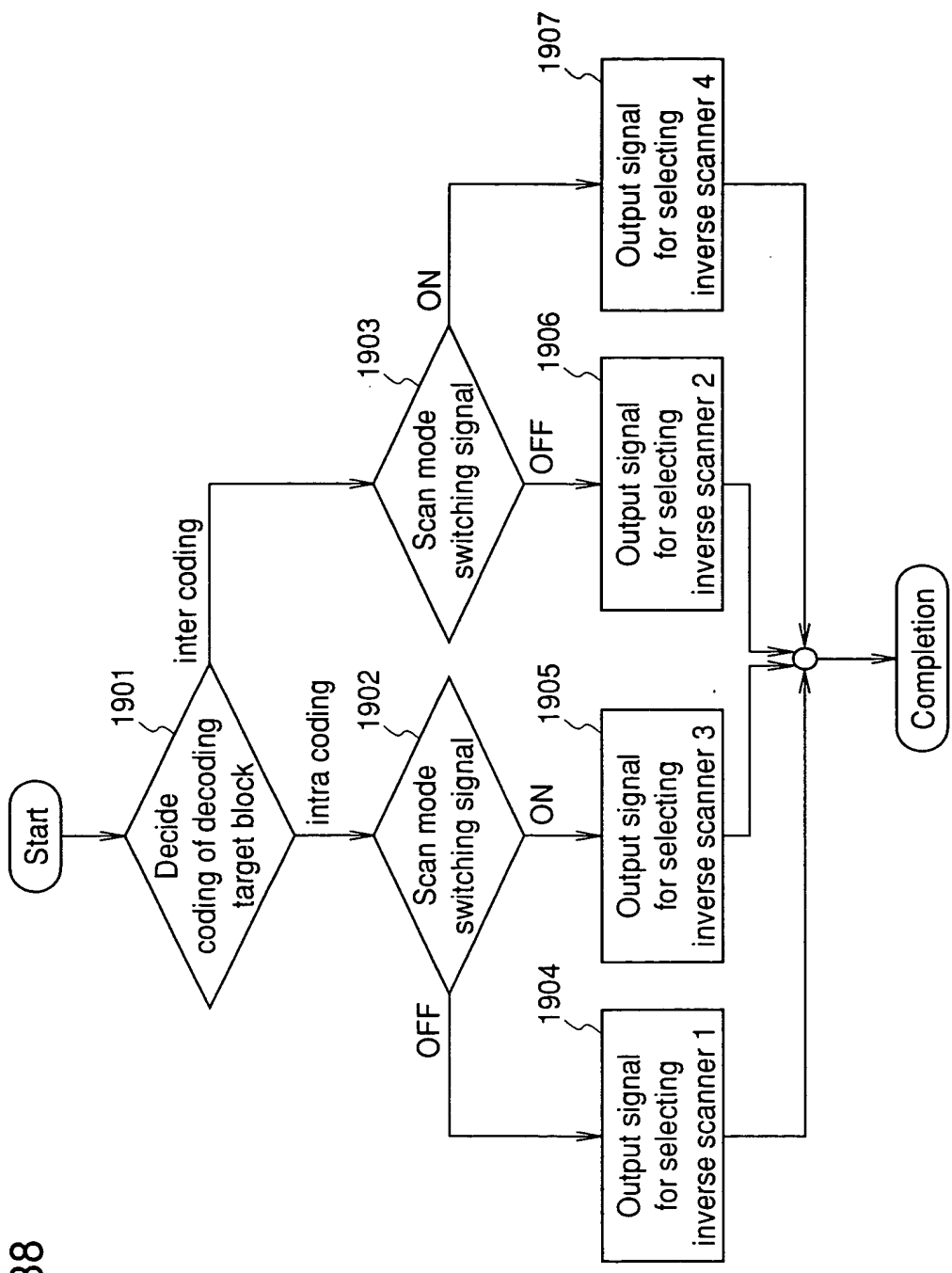


Fig.38

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